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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTORNEY'S DOCKET NUMBER
P/61728-PCT

**TRANSMITTAL LETTER TO THE UNITED STATES
DESIGNATED/ELECTED OFFICE (DO/EO/US)
CONCERNING A FILING UNDER 35 U.S.C. 371**

U.S. APPLICATION NO. (if known, see 37 CFR 1.5)
10/018762

INTERNATIONAL APPLICATION NO.
PCT/GB00/02093

INTERNATIONAL FILING DATE
June 1, 2000

PRIORITY DATE CLAIMED
June 17, 1999

TITLE OF INVENTION **SUPERREGENERATIVE AM DEMODULATOR**

APPLICANT(S) FOR DO/EO/US **Ian James FORSTER, Adrian Nigel FARR**

Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:

1. ☒ This is a **FIRST** submission of items concerning a filing under 35 U.S.C. 371.
2. ☐ This is a **SECOND** or **SUBSEQUENT** submission of items concerning a filing under 35 U.S.C. 371.
3. ☒ This is an express request to begin national examination procedures (35 U.S.C. 371 (f)). The submission must include items (5), (6), (9) and (21) indicated below.
4. ☐ The US has been elected by the expiration of 19 months from the priority date (Article 31).
5. ☒ A copy of the International Application as filed (35 U.S.C. 371(c)(2))
 - a. ☐ is attached hereto (required only if not communicated by the International Bureau).
 - b. ☒ has been communicated by the International Bureau.
 - c. ☐ is not required, as the application was filed in the United States Receiving Office (RO/US).
6. ☐ An English language translation of the International Application as filed (35 U.S.C. 371(c)(2)).
 - a. ☐ is attached hereto.
 - b. ☐ has been previously submitted under 35 U.S.C. 154(d)(4).
7. ☒ Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3)).
 - a. ☐ are attached hereto (required only if not communicated by the International Bureau).
 - b. ☒ have been communicated by the International Bureau.
 - c. ☐ have not been made; however, the time limit for making such amendments has NOT expired.
 - d. ☐ have not been made and will not be made.
8. ☐ An English language translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).
9. ☐ An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)).
10. ☐ An English language translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)).

Items 11 to 20 below concern document(s) or information included:

11. ☐ An Information Disclosure Statement under 37 CFR 1.97 and 1.98.
12. ☐ An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.
13. ☒ A **FIRST** preliminary amendment.
14. ☐ A **SECOND** or **SUBSEQUENT** preliminary amendment.
15. ☐ A substitute specification.
16. ☐ A change of power of attorney and/or address letter.
17. ☐ A computer-readable form of the sequence listing in accordance with PCT Rule 13ter.2 and 35 U.S.C. 1.821 - 1.825.
18. ☐ A second copy of the published international application under 35 U.S.C. 154(d)(4).
19. ☐ A second copy of the English language translation of the international application under 35 U.S.C. 154(d)(4).
20. ☒ Other items or information: **Receipt Acknowledgment Postcard**

U.S. APPLICATION NO. (if known, see 37 CFR 1.53) <div style="font-size: 1.5em; font-weight: bold;">10/018762</div>		INTERNATIONAL APPLICATION NO. PCT/GB00/02093		ATTORNEY'S DOCKET NUMBER P/61728-PCT	
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21. <input checked="" type="checkbox"/> The following fees are submitted: BASIC NATIONAL FEE (37 CFR 1.492 (a) (1) - (5)) : Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2)) paid to USPTO and International Search Report not prepared by the EPO or JPO \$1,040.00 International preliminary examination fee (37 CFR 1.482) not paid to USPTO but International Search Report prepared by the EPO or JPO \$890.00 International preliminary examination fee (37 CFR 1.482) not paid to USPTO but international search fee (37 CFR 1.445(a)(2)) paid to USPTO \$740.00 International preliminary examination fee (37 CFR 1.482) paid to USPTO but all claims did not satisfy provisions of PCT Article 33(1)-(4) \$710.00 International preliminary examination fee (37 CFR 1.482) paid to USPTO and all claims satisfied provisions of PCT Article 33(1)-(4) \$100.00 <div style="text-align: center;">ENTER APPROPRIATE BASIC FEE AMOUNT =</div>				CALCULATIONS PTO USE ONLY <div style="border: 1px solid black; padding: 5px;"> <div style="display: flex; justify-content: space-between;"> \$890.00 </div> <div style="display: flex; justify-content: space-between;"> \$0.00 </div> </div>	
Surcharge of \$130.00 for furnishing the oath or declaration later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492 (e)).				<div style="border: 1px solid black; padding: 5px;"> <div style="display: flex; justify-content: space-between;"> \$0.00 </div> </div>	
CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE	\$	
Total claims	18 - 20 =	0	x \$18.00	\$0.00	
Independent claims	8 - 3 =	5	x \$84.00	\$420.00	
MULTIPLE DEPENDENT CLAIM(S) (if applicable)			+ \$280.00	\$0.00	
TOTAL OF ABOVE CALCULATIONS =				\$1,310.00	
<input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27. The fees indicated above are reduced by 1/2.				<div style="border: 1px solid black; padding: 5px;"> <div style="display: flex; justify-content: space-between;"> \$0.00 </div> </div>	
SUBTOTAL =				\$1,310.00	
Processing fee of \$130.00 for furnishing the English translation later than <input type="checkbox"/> 20 <input type="checkbox"/> 30 months from the earliest claimed priority date (37 CFR 1.492(f)).				<div style="border: 1px solid black; padding: 5px;"> <div style="display: flex; justify-content: space-between;"> \$0.00 </div> </div>	
TOTAL NATIONAL FEE =				\$1,310.00	
Fee for recording the enclosed assignment (37 CFR 1.21 (h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31). \$40.00 per property +				<div style="border: 1px solid black; padding: 5px;"> <div style="display: flex; justify-content: space-between;"> \$0.00 </div> </div>	
TOTAL FEES ENCLOSED =				\$1,310.00	
				Amount to be refunded:	\$
				charged:	\$

a. ☒ A check in the amount of **\$1,310.00** to cover the above fees is enclosed.

b. ☐ Please charge my Deposit Account No. _____ in the amount of \$_____ to cover the above fees. A duplicate copy of this sheet is enclosed.

c. ☒ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 11-1145. A duplicate copy of this sheet is enclosed.

NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137 (a) or (b)) must be filed and granted to restore the application to pending status.

SEND ALL CORRESPONDENCE TO:

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SIGNATURE:

Alan Israel
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27,564
 REGISTRATION NUMBER

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as Express Mail No. EL 337 911 768 US in an envelope addressed to: Box PCT, Commissioner of Patents and Trademarks, Washington, D.C., 20231, on: December 17, 2001 (date)

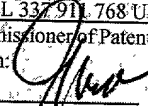
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10/018762

531 Rec'd PCT/PT 17 DEC 2001

Docket No.: P/61728

PATENTS
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Alan Ismael
Reg. No. 27,564

International Application No.: PCT/GB00/02093
International Filing Date : June 1, 2000
In re: Application of : Ian James FORSTER
Deposited : December 17, 2001
For : SUPERREGENERATIVE AM DEMODULATOR

New York, New York
December 17, 2001

PRELIMINARY AMENDMENT

BOX: PCT
Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

Prior to calculation of the filing fee and before examination, kindly amend the above captioned application as follows:

IN THE CLAIMS:

Please cancel claims 1-18, without prejudice.

Please add the new set of claims set forth on the enclosed pages.

IN THE ABSTRACT:

Delete the "Abstract" on the PCT cover sheet and replace it with the "Abstract of the Disclosure" set forth on a separate sheet attached hereto.

REMARKS

An abstract has been provided on a separate sheet; and the claims have been amended to conform to U.S. practice.

Wherefore, an early action on the merits is earnestly solicited.

Respectfully submitted,

KIRSCHSTEIN, OTTINGER, ISRAEL & SCHIFFMILLER, P.C.

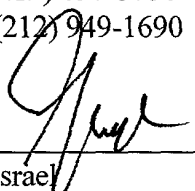
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PROPOSED NEW CLAIMS

19. An amplitude modulation receiver for receiving an input signal and generating a corresponding demodulated signal, comprising: a transistor biased to be simultaneously operable as a reflection amplifier for reflectively amplifying the input signal, and as a detector for detecting an amplified input signal to generate the demodulated signal.

20. The receiver according to claim 19, wherein the transistor is operative in a non-linear region of its current/voltage transfer characteristic.

21. The receiver according to claim 20, wherein the transistor is operable to conduct a current therethrough in a range of 5 μ A to 110 μ A to function in said non-linear region.

22. The receiver according to claim 19, wherein the transistor incorporates an electrode for receiving the input signal, the electrode being connected through a signal path to a signal earth such that the path is operable for conveying reflected signals between the transistor and the signal earth, and for diverting the input signal to the electrode.

23. The receiver according to claim 19, and an antenna assembly for receiving input radiation and generating therefrom the input signal for the transistor.

24. The receiver according to claim 19, wherein the receiver has a gain which is responsive to a magnitude of the input signal, thereby providing the receiver with an automatic gain control characteristic.

25. A frequency modulation (FM) receiver, comprising: an amplitude modulation (AM) receiver for receiving an input signal and generating a corresponding demodulated signal, including a transistor biased to be simultaneously operable as a reflection amplifier for reflectively amplifying the input signal, and as a detector for detecting an amplified input signal to generate the

demodulated signal; and converting means for converting an input frequency modulated signal applied thereto into a corresponding amplitude modulated signal, the AM receiver being operable for demodulating the amplitude modulated signal to provide a demodulated output signal.

26. The receiver according to claim 25, wherein the converting means comprises a bandpass filter operable off resonance for converting the frequency modulated signal into the corresponding amplitude modulated signal.

27. A global positioning satellite (GPS) receiver, comprising: a modulation receiver for receiving an input signal and generating a corresponding demodulated signal, including a transistor biased to be simultaneously operable as a reflection amplifier for reflectively amplifying the input signal, and as a detector for detecting an amplified input signal to generate the demodulated signal; receiving means for receiving input radiation and for generating a corresponding received signal; and processing means for filtering, amplifying and gating the received signal to provide input signals for the modulation receiver to demodulate to provide demodulated signals from which a positional reference for the GPS receiver is derivable.

28. The receiver according to claim 27, wherein the receiving means is a circularly polarized antenna.

29. The receiver according to claim 27, wherein the processing means incorporates reflection amplifiers for amplifying and gating the received signal for generating processed signals.

30. The receiver according to claim 27, wherein the processing means incorporates magnetostatic filtering and frequency selective limiting means for processing the received signal.

31. An identification tag, comprising: a modulation receiver for receiving an input signal and generating a corresponding demodulated signal, including a transistor biased to be simultaneously operable as a reflection amplifier for reflectively amplifying the input signal, and as a detector for detecting an amplified input signal to generate the demodulated signal, the modulation receiver being operable to be responsive to radio radiation received thereat.

32. A wireless local area network for interconnecting computers, the network comprising: a modulation receiver for receiving an input signal and generating a corresponding demodulated signal, including a transistor biased to be simultaneously operable as a reflection amplifier for reflectively amplifying the input signal, and as a detector for detecting an amplified input signal to generate the demodulated signal, the modulation receiver being operative for performing demodulation of signals within the network.

33. A mobile telephone, comprising: a modulation receiver for receiving an input signal and generating a corresponding demodulated signal, including a transistor biased to be simultaneously operable as a reflection amplifier for reflectively amplifying the input signal, and as a detector for detecting an amplified input signal to generate the demodulated signal, the modulation receiver being operable to provide demodulation of signals propagating therein.

34. An electronic security key, comprising: a modulation receiver for receiving an input signal and generating a corresponding demodulated signal, including a transistor biased to be simultaneously operable as a reflection amplifier for reflectively amplifying the input signal, and as a detector for detecting an amplified input signal to generate the demodulated signal, the modulation receiver being operative for performing demodulation of signals propagating therein.

35. The key according to claim 34, wherein the receiver is housed within a key fob.

36. A method of amplitude demodulating an input signal, the method comprising the simultaneously executable steps of:

- a) biasing a transistor to be simultaneously operable as a reflection amplifier and as a detector;
- b) receiving the input signal and reflectively amplifying the input signal in the transistor operating as the reflection amplifier to generate an amplified input signal; and
- c) passing the amplified input signal through the transistor operating as the detector in a non-linear mode to demodulate the amplified input signal and thereby generate a corresponding demodulated signal.

SUPERREGENERATIVE AM DEMODULATOR

The present invention relates to an amplitude modulation (AM) receiver for amplifying and detecting amplitude modulated radiation and signals.

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Conventional receivers operable to receive amplitude modulated radiation and generate corresponding demodulated signals, for example receivers such as domestic radios, often incorporate a number of sections; these sections conventionally comprise an antenna for receiving the radiation and generating a corresponding received signal, transmission radio frequency (r.f.) amplifiers for amplifying and filtering the received signal to provide an amplified signal and a detector for demodulating the amplified signal to provide a demodulated output signal. These sections consume significant power when operational, even when operating at reduced power in stand-by mode awaiting incoming radiation, for example awaiting radiation bearing "wake-up-codes" in mobile telephones. This significant power consumption is a problem which limits operating time from a given set of batteries supplying power to the sections. This problem also pertains to radio transponder tags incorporating miniature power cells to provide electrical power thereto, for example as incorporated into identity tags and electronic access keys for automobiles.

A further problem arises in conventional receivers where the received signal is often in the order of microvolts and where considerable amplification is required to provide amplified signals of sufficient magnitude to operate detection diodes in detectors of the receivers; such detectors often exhibit cutoff voltages which prevent them from detecting signals presented thereto below a minimum threshold amplitude. Because there is a practical limit to a degree of amplification that can be provided without risking spurious oscillations arising, this practical limit imposes a lower threshold amplitude for received radiation, and hence a

limited operating range, to which the receivers are responsive. This lower threshold amplitude is a problem in some applications, especially where more remote receiver operation is contemplated.

- 5 In conventional receivers, it is a well known principle that an r.f. transmission amplifier providing a non-linear transfer characteristic can be used as a demodulator for demodulating amplitude modulated signals. This principle was frequently employed in the context of thermionic electron tubes incorporated into radio receivers before a time when silicon semiconductor devices became widely available, the electron tubes functioning as transmission amplifiers.

The inventor has appreciated that it is feasible to combine a number of radio receiver sections together to obtain a simplified receiver circuit which counteracts one or more of the problems described above.

- 15 According to a first aspect of the present invention, there is provided an AM receiver for receiving an input signal and generating a corresponding demodulated signal, characterised in that the receiver incorporates a transistor biased to be simultaneously operable as a reflection amplifier for reflectively amplifying the input signal and as a detector for detecting the amplified input signal to generate the demodulated signal.

The invention provides the advantage that the receiver is capable of:

- (a) consuming less power on account of exploiting reflective amplification, in contrast to transmission amplification employed in the prior art; and
- 25 (b) being more sensitive to the input signal because detection diodes imposing a lower response threshold are not incorporated therein.

Conveniently, detection of the amplified input signal occurs by the transistor providing signal mixing therein. Advantageously, the transistor is operable in a non-linear region of its current/voltage transfer characteristic. This provides the advantage that the amplified
5 input signal is mixed with itself, namely "autoheterodyned", to demodulate it directly to baseband to provide the demodulated signal.

Advantageously, the transistor functions in its non-linear region at a relatively low supply current, thereby making the receiver more power efficient. Thus, for example, the transistor
10 can be operable to conduct a current therethrough in a range of 5 μ A to 100 μ A when functioning in its non-linear region.

In order for the transistor to function simultaneously as a reflection amplifier and as a detector, the transistor is preferably configured so that amplified reflected signals are
15 generated at an input of the transistor for subsequent detection in the transistor; the transistor presents a negative input resistance at its input to achieve this function. In contrast, conventional non-linear mixers employing transmission amplifiers do not generate amplified signals at their inputs and do not then inject them back for demodulation purposes.

Conveniently, the transistor incorporates an electrode for receiving the input signal, the
20 electrode being connected through a signal path to a signal earth such that the path is operable to convey reflected signals between the transistor and the signal earth, and to divert the input signal to the electrode.

According to a second aspect of the invention, receivers according to the first aspect of the
25 invention can be incorporated into a GPS receiver, thereby providing it with relatively reduced operating current consumption and increased detection sensitivity. Conveniently,

the GPS receiver incorporates a plurality of receivers according to the invention, receiving means for receiving input radiation and generating a corresponding received signal, and processing means for filtering, amplifying and gating the received signal to provide input signals for the plurality of receivers to demodulate to provide demodulated signals from which a positional reference for the GPS receiver is derivable.

According to a third aspect of the invention, the invention provides a method of amplitude demodulating an input signal using an AM receiver according to the first aspect, the method comprising the simultaneously executable steps of:

- (a) receiving the input signal and reflectively amplifying it in the transistor to generate an amplified input signal; and
- (b) passing the amplified input signal through the transistor operating in a non-linear mode to demodulate it and thereby generate a corresponding demodulated signal.

Embodiments of the invention will now be described, by way of example only, with reference to the following diagrams in which:

Figure 1 is a schematic diagram of a first embodiment of an AM receiver according to the invention; and

Figure 2 is a schematic illustration of a GPS receiver incorporating the receiver shown in Figure 1.

Referring to Figure 1, there is illustrated an AM receiver indicated by 10. The receiver 10 incorporates a patch antenna 12, a gallium arsenide (GaAs) field effect transistor (FET) 14, a current source 16 and a power source 18. The antenna 12 is connected to a gate electrode

14g of the transistor 14 and also through a link 20 to a signal earth Eg. The power source 18 incorporates an output P connected to a drain electrode 14d of the transistor 14. The transistor 14 also incorporates a source electrode 14s which is connected to a first terminal I1 of the current source 16. A second terminal I2 of the source 16 is also connected to the
5 signal earth Eg. The link 20 and the terminal I2 are both connected at a single point onto the signal earth Eg.

The source electrode 14s is also connected to an output Q whereat a demodulated signal is provided from the receiver 10.

10 Operation of the receiver 10 will now be described with reference to Figure 1. The transistor 14 is in a grounded gate configuration so that a positive bias provided by the power supply 18 at its output P causes a current I_{FET} in the order of microamperes to flow between the electrodes 14d, 14s and through the current source 16. Preferably, I_{FET} is in a range of 5 μA to 100 μA . The current I_{FET} is sufficiently small for the transistor 14 to operate as a
15 reflection amplifier in a non-linear region of its transfer characteristics. It has been experimentally verified that the transistor 14 provides reflective amplification by measuring a signal gain provided by the receiver 10; it is not possible for the receiver 10 to provide such high amplification if the transistor 14 were functioning as a transmission amplifier. The
20 current source 16 is operable to maintain the transistor 14 biased in the non-linear region.

The antenna 12 receives incoming radiation 22 and converts it to a received signal S_R . The signal S_R propagates from the antenna 12 to the gate electrode 14g and through the link 20.

The link 20 provides an approximately quarter wavelength signal path between the signal
25 earth Eg and the gate electrode 14g at the carrier frequency of the radiation 22; as a consequence, the link 20 does not divert the received signal S_R from the gate electrode 14g

because a low impedance presented by the link 20 where it connects to the earth E_g is transferred to an open circuit at the gate electrode 14g. The link 20 also functions as an impedance matching component; its length can be trimmed to afford an impedance match between the transistor 14 and the antenna 12.

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The transistor 14 operates on the signal S_R in a two stage process, namely the transistor 14:

- (a) amplifies the received signal S_R at its gate electrode 14g by reflection to generate an reflectively amplified signal S_A thereat; and then
- (b) demodulates the signal S_A , by virtue of the transistor 14 providing a non-linear transfer characteristic, to output a demodulated signal at the source electrode 14s.

The demodulated signal propagates to the output Q for subsequent processing.

In practice, the two stages (a) and (b) occur simultaneously.

In contrast to this two stage process, prior art demodulators do not provide in-situ reflective amplification and simultaneous detection and are therefore relatively less sensitive in comparison to the receiver 10. In contradistinction to the receiver 10, conventional demodulators employing gain devices operating non-linearly to provide amplitude demodulation incorporate the devices configured to function as transmission amplifiers.

In its reflection mode of operation, the transistor 14 provides a square law signal transfer characteristic according to Equation 1:

$$i_{FET} = k_0 (v_{gs})^2 \quad \text{Eq. 1}$$

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where

i_{FET} = small signal change in I_{FET} ;

v_{gs} = small signal change in potential difference between the gate electrode 14g and source electrode 14s; and

k_0 = a gain constant.

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Assuming to a first approximation that:

$$v_{gs} = k_1 S_R \quad \text{Eq. 2}$$

where k_1 = constant,

and that:

$$S_R = k_2 S_m \sin \omega_R t \quad \text{Eq. 3}$$

where

k_2 = a constant;

S_m = an amplitude modulating signal;

ω_R = angular carrier frequency of the radiation 22; and

t = time,

then from Equations 1 to 3:

$$i_{FET} = k_0 k_1^2 k_2^2 S_m^2 \sin^2 \omega_R t = \frac{1}{2} k_0 k_1^2 k_2^2 S_m^2 (1 - \cos 2 \omega_R t) \quad \text{Eq. 4}$$

If high frequency components, namely at the angular frequency ω_R and higher, are filtered

out from the output Q, then the output will be according to Equation 5:

$$Q = k_3 S_m^2 \quad \text{Eq. 5}$$

5 where k_3 = a constant, namely $k_3 = k_0 k_1^2 k_2^2$.

In practice, filtration of the high frequency components will occur in circuits connected to the receiver 10, for example audio amplifiers exhibiting a bandwidth from 50 Hz to 20 kHz, which are not responsive to such components.

Equation 5 corresponds to a demodulated baseband signal at the output Q. Thus, the transistor 14 is capable of providing signal amplification at low operating currents and also providing direct demodulation to baseband.

15 If the signal S_m is digital binary data, non-linearity implicit in Equation 5 is inconsequential.

The receiver 10 provides the advantages that:

- (i) it operates on relatively low currents, for example μA ;
- 20 (ii) it provides both amplification and detection directly from high frequencies to baseband;
- (iii) it is capable of operating with signals of relatively lower amplitude than conventional receivers; and
- (iv) it is potentially cheap and requires few component parts.

25 The advantages make the receiver 10 attractive for use in short distance radio links such as

wireless local area computer networks, for remotely interrogatable identification tags, and for use as enhanced performance detectors in mobile telephones and in global positioning system (GPS) receivers.

- 5 The receiver 10 provides a synergistic advantage of reflective amplification and detection using a single transistor. Moreover, the receiver 10 is capable of operating at microwave frequencies, for example around 1.5 GHz; experimental results at these frequencies have demonstrated that it can amplify and detect amplitude modulated radiation giving rise to a received signal from the antenna 12 in the order of -80 dBm. The results demonstrate that
- 10 the receiver 10 is a considerable improvement compared to conventional diode-type detectors which cannot detect such small received signals without considerable transmission amplification being employed to amplify the received signals prior to diode detection.

Applications of the receiver 10 include use as:

- 15 (a) low power receivers for multimode tag devices, for example personnel wearable tags.

Conventional multimode tags are capable of transmitting messages in an active mode at a greater range than they are capable of receiving messages in a semi-passive mode. The active mode has the disadvantage that higher levels of power consumption are associated therewith compared to the semi-passive mode.

Conventionally, tag power consumption can be reduced by sampling techniques where the tags enter their active mode for a relatively short period at intervals; use of these techniques results in complex operating protocols for the tags. The receiver

20 10 is capable of providing, when incorporated into a multimode tag, a highly sensitive "wake-up" receive capability which provides the tag with an enhanced operating range and reduces the need for complex operating protocols;

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(b) security receivers for automobiles. Such security receivers incorporating the receiver 10 are capable of:

- (1) exhibiting lower power consumption, for example consuming in the order of tens of μW ;
- (2) being of lower cost; and
- (3) operating at ultra high frequencies (UHF) around 500 MHz to 600 MHz and above compared to conventional security receivers.

Security receivers incorporating the receiver 10 can be operative to communicate security codes, for example "radio keys" allowing authorised access to vehicles and control of immobilisation systems incorporated therein. The receiver 10 is sufficiently simple and compact to incorporate into a key fob, thereby enabling bi-directional communication links to be provided between vehicles and their associated key fobs. Such bi-directional links enable more complex security protocols to be used, thus enhancing security, and permits further added value functions to be added in the fobs, for example memorising an associated vehicle's parking position in a car park incorporating a constellation of low power radio transmitters from which the fobs can derive positional references; and

(c) ultra low power GPS receivers.

GPS receivers are well known for use in determining positional references on the earth's surface. A conventional GPS receiver functions by transmitting encoded radiation at a number of carrier frequencies in the order of 1.5 GHz to a constellation of equatorial geostationary satellites and measuring time durations for the radiation to reach the satellites, to be amplified therein and then transmitted therefrom and be received back at the GPS

receiver. Each satellite is responsive to radiation in a particular frequency range associated therewith. Distances corresponding to the time durations are calculated in the GPS receiver and geometrical computations then applied to determine a positional reference for the receiver from the distances because the positions of the satellites are known in advance. The radiation emitted by the GPS receiver is encoded by a pseudo-random bit sequence generated within the receiver; this encoding enables the GPS receiver to recognise its radiation transmitted back from the satellites.

Determination of time duration is achieved in the GPS receiver by correlating "early", "on time" and "late" versions of the pseudo-random bit sequence with radiation received back at the GPS receiver. This is necessary in order to ensure signal synchronisation and hence achieve a reliable and accurate time duration measurement.

Referring now to Figure 2, there is shown a GPS receiver indicated by 100. The receiver 100 incorporates a circularly polarised antenna 110, a magnetostatic surface wave device (MSWD) filter/isolator 120, a reflection amplifier 130, a three-way splitter unit 140, an assembly of reflection amplifiers 150, an assembly of narrowband surface acoustic wave filters 160, and an assembly of receivers 170. The assembly 170 incorporates three receivers 172, 174, 176 which are each identical to the receiver 10 in Figure 1. The receiver 100 also incorporates a measurement unit 178 for processing "late", "on time" and "early" outputs generated by the assembly 170 and generating signals at terminals J_1 , J_2 , J_3 for use in the assembly 150. Moreover, the receiver 100 additionally incorporates a computation unit 180 for determining a positional reference by triangulation from time measurements provided by the measurement unit 178 to provide the positional reference as data at a terminal M_0 of the unit 180. Design of the measurement unit 178 and the computation unit 178 will be familiar to one ordinary skilled in the art of GPS receiver design.

The reflection amplifier 130 and the assembly 150 incorporate reflection amplifiers according to our UK patent GB 2 284 323 B whose contents are hereby incorporated by reference with regard to transistors functioning as reflection amplifiers when operating in a linear region of their transfer characteristics. The reflection amplifiers each comprise a field effect transistor (FET), namely a silicon junction FET (JFET) or a gallium arsenide (GaAs) device, configured by means of a feedback arrangement such as to operate within a linear region of its current/voltage characteristic such that it reflects a signal received thereat with an increased magnitude.

The antenna 110 incorporates an output S_A which is connected to a first input terminal H_1 of the filter/isolator 120. The filter/isolator 120 incorporates a second terminal H_2 which is connected to an input/output terminal F of the amplifier 130, and also a third terminal H_3 which is connected to an input terminal K_0 of the splitter unit 140.

The unit 140 comprises three output terminals K_1, K_2, K_3 connected to input terminals B_1, B_2, B_3 of reflection amplifiers 152, 154, 156 respectively. The reflection amplifiers 152, 154, 156 are incorporated into the assembly 150. The amplifiers 152, 154, 156 also incorporate control terminals C_1, C_2, C_3 to which "late", "on time" and "early" control signals are connected from corresponding terminals J_1, J_2, J_3 of the measurement unit 178 respectively.

These control signals are identical pseudo-random bit streams, namely sequences of data, which are time shifted with respect to one another, usually by a period corresponding to $\frac{1}{2}$ or 1 bit duration in the sequences.

The terminals K_1, K_2, K_3 are also connected to inputs G_1, G_2, G_3 of surface acoustic wave (SAW) filters 162, 164, 166 respectively. The filters 162, 164, 166 incorporate outputs $W_1,$

W₂, W₃ which are connected to inputs Z₁, Z₂, Z₃ of the receivers 172, 174, 176 respectively.

The receivers 172, 174, 176 include outputs D₁, D₂, D₃ at which "late", "on time" and "early" signals are output respectively. These outputs D₁, D₂, D₃ are connected to corresponding inputs E₁, E₂, E₃ of the measurement unit 178 respectively.

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The filter/isolator 120 incorporates a thin layer, namely in a thickness range of 10 μm to 100 μm, of yttrium iron garnet (YIG) deposited onto an aluminum or quartz substrate, which provides a signal propagation path through the isolator 120.

10 The SAW filters 162, 164, 166 are operable to delay signals propagating therethrough and also bandpass filter them. The amplifier 130 is arranged to amplify signals received thereat and subsequently reflected therefrom by +23 dB. The receivers 172, 174, 176 are operable to provide +20 dB amplification gain as well as amplitude modulation detection as described above with reference to the receiver 10. The amplifiers 152, 154, 156 are switchable at their
15 terminals C₁, C₂, C₃ respectively so that each provides +20 dB gain in a high gain state and -20 dB gain in a low gain state.

Operation of the GPS receiver 100 will now be described with reference to Figure 2.

20 The GPS receiver 100 emits from its transmitter units (not shown) connected to the measurement unit 178 encoded radiation to a first geostationary satellite (not shown) which amplifies and transmits the encoded radiation to provide input radiation 190 which is incident on the antenna 110.

25 The antenna 110 receives the input radiation 190, the radiation being C code GPS radiation at 1574.42 MHz frequency, and generates a signal S₁ corresponding thereto. The signal S₁

propagates to the first terminal H_1 of the filter/isolator 120 through which it passes to the second terminal H_2 whereat it is output to the terminal F of the amplifier 130; the filter/isolator 120 is operative to selectively limit components of the signal S_1 whose amplitude exceeds a threshold level, the level determined during manufacture of the filter/isolator 120. The amplifier 130 functions as a negative resistance and reflectively amplifies the signal S_1 to generate a corresponding amplified signal S_2 which propagates from the terminal F back to the terminal H_2 of the filter/isolator 120. The signal S_2 propagates from the terminal H_2 to the terminal H_3 at the filter/isolator 120 whereat it is output as a signal S_3 . The signal S_3 propagates to the input K_0 of the splitter unit 140 which divides the signal S_3 to generate three signals S_{10} , S_{11} , S_{12} at the outputs K_1 , K_2 , K_3 respectively. In response to the control signals applied to the terminals C_1 , C_2 , C_3 , the amplifiers 152, 154, 156 selectively reflectively amplify or attenuate the signals S_{10} , S_{11} , S_{12} respectively; thus, the signals S_{10} , S_{11} , S_{12} thereby become time gated such as to assume a prominent amplitude when correlation thereof with one or more of the control signals occurs.

The signals S_{10} , S_{11} , S_{12} , switched by the amplifiers 152, 154, 156 respectively to be amplified or attenuated, propagate to the inputs G_1 , G_2 , G_3 respectively. The filters 162, 164, 166 bandpass filter and delay the signals S_{10} , S_{11} , S_{12} propagating therethrough to provide corresponding signals S_{20} , S_{21} , S_{22} at the outputs W_1 , W_2 , W_3 respectively. The inputs Z_1 , Z_2 , Z_3 receive the signals S_{20} , S_{21} , S_{22} which pass to the receivers 172, 174, 176 whereat they are amplified and demodulated to provide "late", "on time" and "early" output signals at the outputs D_1 , D_2 , D_3 respectively. The signals D_1 , D_2 , D_3 propagate to the inputs E_1 , E_2 , E_3 respectively of the measurement unit 178.

The unit 178 monitors the inputs E_1 , E_2 , E_3 to identify signals thereat corresponding to

correlation of the signals present at the terminals K_1 , K_2 , K_3 with those generated within the measurement unit 178 and output via the terminals J_1 , J_2 , J_3 to the terminals C_1 , C_2 , C_3 respectively. By doing so, the measurement unit 178 determines, by an iterative process, a time duration for radiation emitted from the receiver 100 to propagate to the first satellite
5 and be received again by the receiver 100.

The receiver 100 repeats measurements as described above for second and third geostationary satellites to derive three time duration measurements. The measurement unit 178 outputs these measurements to the computation unit 180 which calculates therefrom
10 corresponding distances from which a positional reference is calculated by a process of triangulation to provide positional data at an output M_0 .

In broad overview, the control signals applied to the terminals C_1 , C_2 , C_3 are used to gate the signal S_1 from the antenna 110 to provide the output signals at the outputs D_1 , D_2 , D_3 which
15 are processed by the units 178, 180 for determining the time durations and hence a positional reference for the receiver 100. Determination of the positional reference will be familiar to one ordinarily skilled in the art of GPS receiver design where the output signals at the outputs D_1 , D_2 , D_3 are used to steer a code generator incorporated into the measuring unit 178 for use in synchronisation and hence determining the time durations.

The GPS receiver 100 is capable of operating with lower power consumption compared to conventional GPS receivers. The receiver 100 achieves this lower consumption by exploiting reflective amplification in the amplifier 130, in the assembly of reflection
20 amplifiers 150 and in the assembly of receivers 170. Exploitation of reflective amplification provides an additional advantage of allowing the receiver 100 to be fabricated into a GaAs microwave monolithic integrated circuit (MMIC) connected to associated MSWD and SAW

devices. Such a MMIC is potentially a relatively low-cost compact component for incorporation into GPS systems.

It will be appreciated by those skilled in the art that variations may be made to the receiver 10 and to the GPS receiver 100 described in the foregoing without departing from the scope of the invention. For example, the link 20 can be replaced by a filter network operative to bias the transistor 14 to provide reflective gain whilst imparting a narrower passband characteristic to the receiver 10, thereby making it more frequency selective. Moreover, the transistor 14 can be a silicon transistor when the circuit 10 is operable to receive radiation at relatively lower frequencies, for example at VHF frequencies in a range of 100 MHz to 150 MHz.

The receiver 10 is operable to inherently additionally provide an automatic gain control (AGC) characteristic for compressing the dynamic range of signals applied to the receiver 10. By appropriate choice of negative resistance exhibited by the transistor 14 at its gate electrode 14g, the current I_{FET} can be arranged to decrease in response to increased input signal strength so that the negative resistance exhibited at the gate electrode 14g changes to provide reduced gain.

By incorporating a frequency-sensitive cutoff filter, for example a bandpass filter, in a signal path between the antenna 12 and the gate electrode 14g, it is feasible to convert the AM receiver 10 into a frequency modulation (FM) receiver for demodulating frequency modulated radiation. The filter is operative, for example slightly off resonance, to provide signal attenuation for signals propagating therethrough in response to frequency change of the signals; such attenuation is manifest as amplitude modulation in signals provided from the filter which the transistor 14 is operative to demodulate. Moreover, because the receiver

10 can be adapted to be an FM receiver, it is also capable of functioning as a phase detector for demodulating phase modulated radiation and signals.

CLAIMS

1. An AM receiver for receiving an input signal (22) and generating a corresponding demodulated signal (Q), characterised in that the receiver (10) incorporates a transistor (14) biased to be simultaneously operable as a reflection amplifier for reflectively amplifying the input signal and as a detector for detecting the amplified input signal to generate the demodulated signal.
2. A receiver according to Claim 1 wherein the transistor (14) is operative in a non-linear region of its current/voltage transfer characteristic.
3. A receiver according to Claim 2 wherein the transistor is operable to conduct a current therethrough in a range of 5 μ A to 100 μ A to function in its non-linear region.
4. A receiver according to Claim 1, 2 or 3 wherein the transistor (14) incorporates an electrode (14g) for receiving the input signal, the electrode connected through a signal path (20) to a signal earth such that the path (20) is operable to convey reflected signals between the transistor and the signal earth, and to divert the input signal to the electrode (14g).
5. A receiver according to Claim 1, 2, 3 or 4 wherein the receiver (10) incorporates an antenna assembly (12) for receiving input radiation (22) and generating therefrom the input signal for the transistor.
6. A receiver according to Claim 1, 2, 3 or 4 arranged to provide a gain therethrough

which is responsive to input signal magnitude, thereby providing the receiver with an AGC characteristic.

7. An FM receiver incorporating an AM receiver according to any one of Claims 1 to 6, the FM receiver further comprising converting means for converting an input frequency modulated signal applied thereto into a corresponding amplitude modulated signal which the AM receiver is operable to demodulate to provide a demodulated output signal.
8. A receiver according to Claim 7 wherein the converting means comprises a band pass filter operable off resonance to convert the frequency modulated signal into the corresponding amplitude modulated signal.
9. A GPS receiver (100) incorporating one or more receivers (170) according to Claim 1, 2, 3, 4, 6 or 7, receiving means (120, 130) for receiving input radiation and generating a corresponding received signal (K_0), and processing means (140, 150, 160, 170, 178, 180) for filtering, amplifying and gating the received signal (K_0) to provide input signals for the one or more receivers (170) to demodulate to provide demodulated signals from which a positional reference for the GPS receiver (100) is derivable.
10. A receiver according to Claim 9 wherein the receiving means is a circularly polarised antenna.
11. A receiver according to Claim 9 or 10 wherein the processing means (140, 150, 160, 170, 178, 180) incorporates reflection amplifiers (172, 174, 176) for amplifying and

gating the received signal for generating the processed signals.

12. A receiver according to Claim 9, 10 or 11 wherein the processing means incorporates magnetostatic filtering and frequency selective limiting means (160) for processing the received signal.
13. An identification tag incorporating a receiver according to any one of Claims 1 to 8 operable to be responsive to radio radiation received thereat.
14. A wireless local area network for interconnecting computers, the network incorporating a receiver according to any one of Claims 1 to 8 for performing demodulation of signals within the network.
15. A mobile telephone incorporating a receiver according to any one or Claims 1 to 8 operable to provide demodulation of signals propagating therein.
16. An electronic security key incorporating a receiver according to any one of Claim 1 to 8 for performing demodulation of signals propagating therein.
17. A key according to Claim 16 wherein the receiver is housed within a key fob.
18. A method of amplitude demodulating an input signal using an AM receiver (10) according to Claim 1, the method comprising the simultaneously executable steps of:
 - (a) receiving the input signal and reflectively amplifying it in the transistor (14) to generate an amplified input signal; and

- (b) passing the amplified input signal through the transistor (14) operating in a non-linear mode to demodulate it and thereby generate a corresponding demodulated signal (Q).

ABSTRACT OF THE DISCLOSURE

An AM receiver incorporates an antenna, a transistor, a current source, and a power supply. The antenna is connected to a gate electrode of the transistor and through a link to a signal earth. In operation, the antenna receives radiation and generates a corresponding input signal which propagates to the gate electrode. The transistor is operable to process the input signal in two steps, namely to reflectively amplify the input signal to generate a correspondingly reflectively amplified input signal at the gate electrode in a first step, and amplitude demodulate the amplified input signal in a second step. The transistor is operable in a non-linear region of its current/voltage characteristic whereas it simultaneously provides reflective amplification and signal demodulation, namely the two steps occur simultaneously. In contradistinction, conventional demodulators employing gain devices operating non-linearly to provide amplitude demodulation incorporate the devices configured to function as transmission amplifiers.

Fig.1.

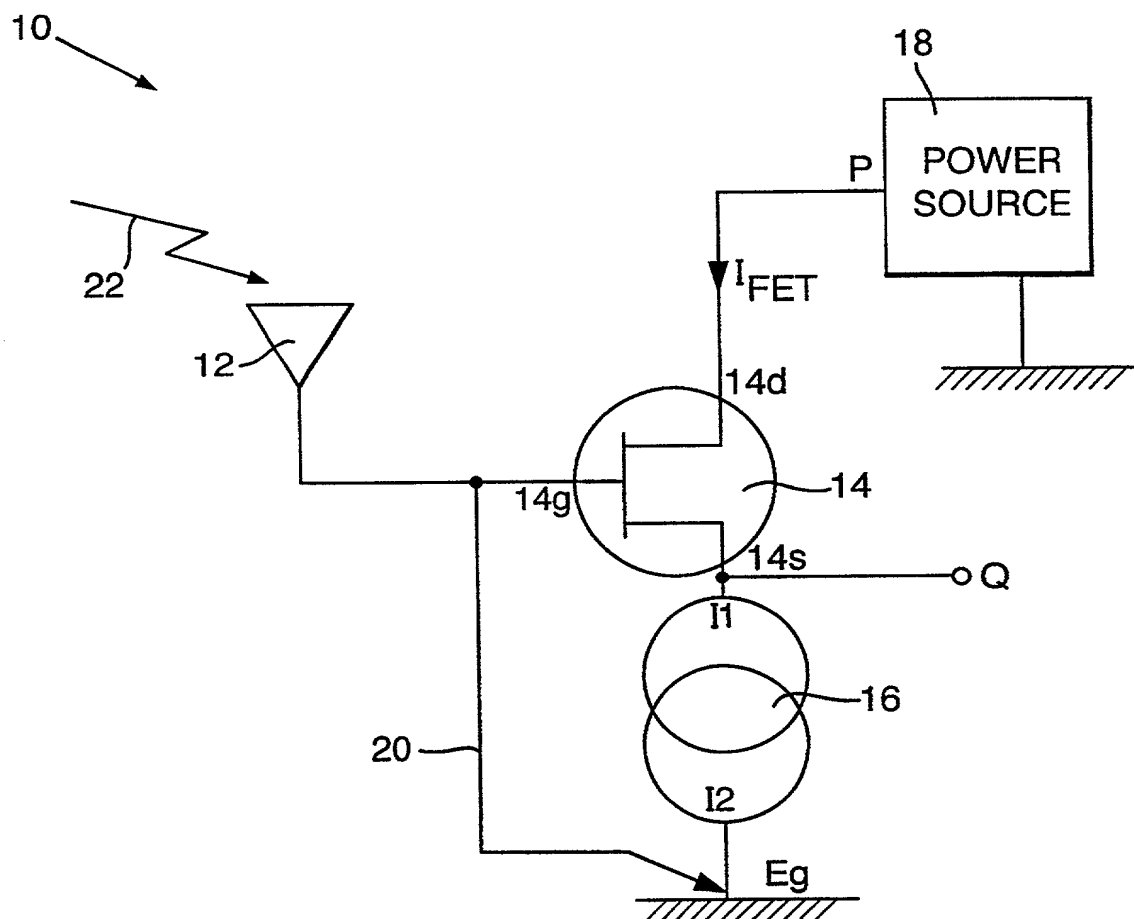


Fig. 2.

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Rev. 6/95U.S. Department of Commerce
Patent and Trademark Office**DECLARATION FOR
UTILITY OR DESIGN
PATENT APPLICATION**

☐ Declaration OR
Submitted
with Initial Filing

☒ Declaration
Submitted after
Initial Filing

Attorney Docket Number

P/61728

First Named Inventor

FORSTER, JAMES

COMPLETE IF KNOWN

Application Number

10/018,762

Filing Date

DECEMBER 17, 2001

Group Art Unit

Examiner Name

As a below named inventor, I hereby declare that:

My residence, post office address, and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

SUPERREGENERATIVE AM DEMODULATOR

(Title of the Invention)

the specification of which

☐ is attached hereto
OR

☒ was filed on (MM/DD/YYYY)

DECEMBER 17, 2001

as United States Application Number or PCT International

Application Number

10/018,762

and was amended on (MM/DD/YYYY)

(if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment specifically referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37 Code of Federal Regulations, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code §119 (a)-(d) or §365(b) of any foreign application(s) for patent or inventor's certificate, or §365 (a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or of any PCT international application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application Number(s)	Country	Foreign Filing Date (MM/DD/YYYY)	Priority Not Claimed	Certified Copy Attached?	
				YES	NO
9913989.1	United Kingdom	06.17.99	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>	<input checked="" type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>
PCT/GB00/02093	INTERNATIONAL	06/01/2000	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>

☐ Additional foreign application numbers are listed on a supplemental priority sheet attached hereto:

I hereby claim the benefit under Title 35, United States Code § 119(e) of any United States provisional application(s) listed below.

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DECLARATION

Page 2

I hereby claim the benefit under Title 35, United States Code §120 of any United States application(s), or §365(c) of any PCT international application designating the United States of America, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code §112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations §1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application.

U.S. Parent Application Number	PCT Parent Number	Parent Filing Date (MM/DD/YYYY)	Parent Patent Number (if applicable)

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☒ List attorney(s) and/or agent(s) name and registration number below.

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Name of Sole or First Inventor:

☐ A petition has been filed for this unsigned inventor

Given Name	Ian	Middle Initial	J	Family Name	FORSTER	Suffix e.g. Jr.	
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Inventor's Signature	<i>X [Signature]</i>	Date	<i>21st January 2002</i>
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Post Office Address			
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☒ Additional inventors are being named on supplemental sheet(s) attached hereto

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DECLARATION

ADDITIONAL INVENTOR(S) Supplemental Sheet

Name of Additional Joint Inventor, if any:										<input type="checkbox"/> A petition has been filed for this unsigned inventor											
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Inventor's Signature												Date		23/1/2002							
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